

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: S. IKEDA, et al.

Serial No: To be assigned

Filed: April 8, 2004

Title: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD
FOR MANUFACTURING THE SAME

Group: n/a

Examiner: n/a

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

April 8, 2004

Sir:

The following amendments and remarks are respectfully submitted at the time of filing the above-identified Rule 53(b) continuation application filed herewith.

Amendments to the Specification

Amendments to the Claims

Remarks are included following the amendments